the highest of the highest degrees of degradation, thereby suppressing local degradation. The storage battery may be any of a unit cell and a battery block including multiple unit cells. The storage battery units including multiple storage batteries may be any of battery blocks and battery modules.

[0068] A system of a sixteenth aspect of the present disclosure may be the following: the system of any one of the first to fifteenth aspects, for example, further includes detectors that detect the quantities of state of the storage battery units, and the controller causes the adjusters to stop the currents flowing through the second circuits and determines the degrees of degradation of the storage battery units from the quantities of state detected by the detectors while stopping the currents flowing through the second circuits.

[0069] Thus, the quantity of state is detected appropriately without being affected by adjustment. As a result, the degree of degradation is determined appropriately.

[0070] A method according to a seventeenth aspect includes performing at least one of first control and second control. The first control includes (a) during a charge of multiple storage battery units connected in series, making a voltage of a first storage battery unit of the storage battery units higher than a voltage of a second storage battery unit of the storage battery units by adjusting the amounts of currents flowing through circuits connected in parallel with the storage battery units, the second storage battery unit having a higher degree of degradation than the degree of degradation of the first storage battery unit, and (b) then stopping the charge of the storage battery units with the voltage of the first storage battery unit higher than the voltage of the second storage battery unit. The second control includes (c) during a discharge of the storage battery units, making the voltage of the first storage battery unit lower than the voltage of the second storage battery unit by adjusting the amounts of the currents flowing through the circuits connected in parallel with the storage battery units and (d) then stopping the discharge of the storage battery units with the voltage of the first storage battery unit lower than the voltage of the second storage battery unit. If the controller performs the first control and the second control, the controller performs the first control and the second control in different times. For example, the controller performs the first control before or after the second control.

[0071] Thus, the degradation of the storage battery unit having the higher degree of degradation is suppressed compared to that of the storage battery unit having the lower degree of degradation. This results in the suppression of the local degradation of the storage battery units and the extension of the life of the entire storage battery units. That is, the storage battery units are controlled appropriately.

[0072] It should be noted that these general or specific aspects may be implemented as systems, devices, methods, integrated circuits, computer programs, non-transitory storage media such as computer-readable CD-ROM, or any combinations thereof.

[0073] Now, embodiments will be described in detail with reference to the accompanying drawings. The embodiments below represent general or specific examples. The numbers, shapes, materials, elements, the positions and connection forms of the elements, steps, the order of the steps, and the like described in the embodiments are only illustrative and are not intended to limit the present disclosure. Of the elements of the embodiments, elements which are not set forth in the independent claims representing the highest concept are described as optional elements.

[0074] For description, ordinal numbers, such as first, second, and third, may be added to the elements or the like, replaced, or removed. A charge/discharge refers to at least one of a charge and a discharge. Voltage, current, resistance, and power may refer to a voltage value indicating voltage, a current value indicating current, a resistance value indicating resistance, and a power value indicating power, respectively.

First Embodiment

[0075] FIG. **1** is a block diagram showing the configuration of a power storage system according to the present embodiment. A power storage system **100** shown in FIG. **1** controls storage battery units **121**, **122**, and **123**. The power storage system **100** may consist of a single device or may consist of multiple devices. The power storage system **100** includes a first circuit **110**, second circuits **131**, **132**, and **133**, and a controller **150**.

[0076] The first circuit **110** is an example of a first circuit of the present disclosure and is an electric circuit in which the storage battery units **121**, **122**, and **123** are connected in series. Specifically, the first circuit **110** corresponds to a path from a terminal **101** to a terminal **102** of the power storage system **100** through the storage battery units **121**, **122**, and **123**. One of the terminal **101** and terminal **102** is a positive electric terminal, and the other is a negative electric terminal.

[0077] The storage battery units **121**, **122**, and **123** are an example of storage battery units of the present disclosure and are elements for accumulating electric energy. The storage battery units **121**, **122**, and **123** may be any of unit cells, battery blocks including multiple unit cells, and battery modules including multiple battery blocks. A unit cell is also called a storage battery or simply called a cell. A battery block is also called a storage battery block. In a battery block, multiple unit cells are connected at least either in series or in parallel. A battery module is also called a storage battery module, a battery pack, or a storage battery pack. In a battery module, multiple battery blocks are connected at least either in series or in parallel.

[0078] The second circuits **131**, **132**, and **133** are electric circuits and are an example of second circuits of the present disclosure. The second circuits **131**, **132**, and **133** are disposed in parallel with the storage battery units **121**, **122**, and **123**. Specifically, the second circuit **131** is disposed in parallel with the storage battery unit **121**; the second circuit **132** is disposed in parallel with the storage battery unit **122**; and the second circuit **133** is disposed in parallel with the storage battery unit **123**.

[0079] For example, the second circuit **131** corresponds to a path from a junction **103** through an adjuster **141** to a junction **104**. The second circuit **132** corresponds to a path from a junction **105** through an adjuster **142** to a junction **106**. The second circuit **133** corresponds to a path from a junction **107** through an adjuster **143** to a junction **108**.

[0080] The adjusters **141**, **142**, and **143** adjust the amounts of currents flowing through the second circuits **131**, **132**, and **133**, respectively. For example, the adjusters **141**, **142**, and **143** are electric circuits included in the second circuits **131**, **132**, and **133**, respectively. Specifically, the adjuster **141** adjusts the amount of the current flowing through the second circuit **131**; the adjuster **142** adjusts the amount of the